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Applicant(s): Gyung-Su-Cho) I hereby certify that this paper is
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For: "Semiconductor Device and a) Box 1450, Alexandria, VA 22313-
Fabrication Method Thereof") 1450 on this date:
Group Art Unit: 2811) March 8, 2004
Examiner: Unknown	Was G Maden
Examiner. Chichewii) Mark G. Hanley
) Registration No. 44,736
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) Attorney for Applicant(s)

INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Sir:

The patents and/or publications listed on the enclosed PTO Form-1449 are submitted pursuant to 37 CFR §§ 1.56, 1.97, and 1.98. Copies of the patents or publications are enclosed.

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By:

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March 8, 2004

Form PTO-1449 (Modified) U.S. Department of	Atty. Docket No.	Serial No.	
Commerce	20067/OPP030889US	10/676,645	
MAR 1 1 2004 Patent and Trademark Office	Applicant		
	Gyung-Su Cho		
INFORMATION DISCLOSURE STATEMENT	Filing Date	Group Art Unit	
(Use several sheets if necessary)	October 1, 2003	Unknown	
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*EVALORES		DOCUMENT	ISSUE	NT DOCUMENTS INVENTOR(S)	CLASS	SUBCLASS	FILING DATE
*EXAMINER INITIALS		NUMBER	DATE	HAVENTOR(3)	CLASS	SOBOLINOS	IF APPROPRIATE
	A01	6,239,494	5/29/2001	Besser et al.			
	A02	6,376,353	4/23/2002	Zhou et al.			
	A03	6,400,026	6/4/2002	Andou et al.			
	A04						
	A05						
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^{*}EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.